

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Withdrawn) A sales method for selling semiconductor devices by way of a communication network, said method comprising:

presenting function-related information and price-related information on partially good semiconductor devices having one or more than one unusable functions to the client terminals by way of the communication network and prompting the client terminals to provide purchase/non-purchase information; and

determining the possibility of successful transactions on the basis of the purchase/non-purchase information provided from client terminals.

2. (Withdrawn) A sales method for selling semiconductor devices by way of a communication network, said method comprising:

presenting function-related information on a semiconductor device to be possibly purchased by way of the communication network; and

providing purchase/non-purchase information on the basis of the function-related information and the price-related information on partially good semiconductor devices having one or more than one unusable functions and the function specified in said function-related information provided by way of the communication network.

3. (Withdrawn) The sales method according to claim 1, wherein
the price-related information on each of the partially good products is defined on
the basis of the trend of the price zone of the market for similar partially good products
obtained by referring to the function-related information of the partially good product.

4. (Withdrawn) The sales method according to claim 2, wherein
the price-related information on each of the partially good products is defined on
the basis of the trend of the price zone of the market for similar partially good products
obtained by referring to the function-related information of the partially good product.

5. (Withdrawn) The sales method according to claim 1, wherein
the lower limit of the price of each of the partially good products is set at a level
not lower than the cost of disposing the partially good product as waste.

6. (Withdrawn) The sales method according to claim 2, wherein
the lower limit of the price of each of the partially good products is set at a level
not lower than the cost of disposing the partially good product as waste.

7. (Withdrawn) The sales method according to claim 2, wherein
each of said partially good products comprises:
a plurality of cell arrays;
a selection control circuit for generating and outputting a selection control signal
to be used for generating internal address signals for selecting at least one of said

plurality of cell arrays; and

an array selection circuit for generating said internal address signals on the basis of an external address signal and said selection control signal and outputting it to said plurality of cell arrays; wherein

said array selection circuit being adapted to define the correspondence of each piece of information contained in said selection control signal and a selectable combination of any of said plurality of cell arrays and output internal address signals for selecting any of the combinations of said plurality of cell arrays.

8. (Withdrawn) A sales system for selling semiconductor devices by way of a communication network, said system comprising:

a products to be possibly purchased registering means for obtaining function-related information on semiconductor devices to be possibly purchased from the client terminals by way of the communication network;

a partially good products presenting means for presenting function-related information and price-related information on partially good semiconductor devices having one or more than one unusable functions and the functions specified in the function-related information obtained by said products to be possibly purchased registering means and prompting the client terminals to provide purchase/non-purchase information; and

a possibility of successful transaction determining means for obtaining purchase/non-purchase information on said partially good products from the client

terminals and determining the possibility of successful transactions on the basis of the purchase/ non-purchase information provided from client terminals.

9. (Withdrawn) The sales system according to claim 8, wherein the price-related information on each of the partially good products is defined on the basis of the trend of the price zone of the market for similar partially good products obtained by referring to the function-related information of the partially good product.

10. (Withdrawn) The sales system according to claim 8, wherein the lower limit of the price of each of the partially good products is set at a level not lower than the cost of disposing the partially good product as waste.

11. (Withdrawn) The sales system according to claim 8, wherein each of said partially good products comprises:
a plurality of cell arrays;
a selection control circuit for generating and outputting a selection control signal to be used for generating internal address signals for selecting at least one of said plurality of cell arrays;
an array selection circuit for generating said internal address signal on the basis of an external address signal and said selection control signal and outputting it to said plurality of cell arrays;
said array selection circuit being adapted to define the correspondence of each piece of information contained in said selection control signal and a selectable

combination of any of said plurality of cell arrays and output internal address signals for selecting any of the combinations of said plurality of cell arrays.

12. (Withdrawn) A sales program product for causing a computer system to sell semiconductor devices by way of a communication network, said product comprising:

a recording medium;

a first means stored in said recording medium and adapted to issue a command to said computer system for obtaining function-related information on the semiconductor devices possibly to buy from the client terminals by way of a communication network;

a second means stored in said recording medium and adapted to display function-related information and price-related information on the partially good semiconductor devices having the functions shown in said function-related information and also unusable functions in response to the function-related information of said semiconductor devices obtained by said products to be possibly purchased registering means; and

a third means stored in said recording medium and adapted to obtain purchase/non-purchase information on each of said partially good products from said client terminals and determining purchase/non-purchase of the product on the basis of said purchase/non-purchase information.

13. (Currently Amended) A semiconductor device comprising:

a plurality of cell arrays;

a selection control circuit for generating and outputting a selection control signal to be used for generating internal address signals ~~for selecting at least one of said plurality of cell arrays; and~~

an array selection circuit for generating said internal address signals on the basis of an external address signal and said selection control signal generated by said selection control circuit and outputting [[it]]said internal address signals to said plurality of cell arrays to select ones of the cell arrays; wherein

~~said array selection circuit being adapted to define the correspondence of each piece of information contained in said selection control signal and a selectable combination of any of said plurality of cell arrays and output internal address signals for selecting any of the combinations of said plurality of cell arrays.~~

14-16. (Cancelled)

17. (Original) The semiconductor device according to claim 13, further comprising:

at least one pad; and wherein

said selection control signal being defined by a combination of binary signals, each representing existence or absence of a state of being bonded of a corresponding one of said pads.

18. (Original) The semiconductor device according to claim 13, further comprising:

at least one fuse; and wherein

said selection control signal being defined by a combination of binary signals, each representing existence or absence of an electrically or physically broken state of a corresponding one of said fuses.

19. (Withdrawn) The semiconductor device according to claim 13, further comprising:

at least one register circuit; and wherein

said selection control signal being defined by a combination of binary signals, each being written in the corresponding one of said register circuits.

20-26. (Cancelled)

27. (New) The semiconductor device according to claim 13, wherein, when all of bits of said selection control signal have the same logic level, said internal address signals are determined only by said external address signal.

28. (New) The semiconductor device according to claim 13, wherein, when bits of said selection control signal have different logic levels, said internal address signals are determined only by said selection control signal.

29. (New) The semiconductor device according to claim 13, wherein said array selection circuit outputs a mode selection signal for identifying a status of the circuit system in terms of having or not having a parity function and a mode selection signal for identify a status of the circuit system in terms of having or not having a function of the cell arrays.